

The block diagram illustrates a feedback control system. An "OUTPUT" signal is fed into a "FEEDBACK CIRCUIT" (labeled 117). The output of the feedback circuit is V_{FB} , which is connected to the input of an "ETA CONTROL" block (labeled 115). The output of the ETA control block is connected to the input of a differential amplifier (labeled 101). The differential amplifier also receives a $V_{SET POINT}$ input. The output of the differential amplifier is connected to the input of a power transistor (labeled 112). The power transistor is connected to a load (labeled 108) and a feedback network (labeled 109, 110, 111). The feedback network is connected to the input of the differential amplifier. The power transistor is also connected to a ground symbol.

FIG. 4

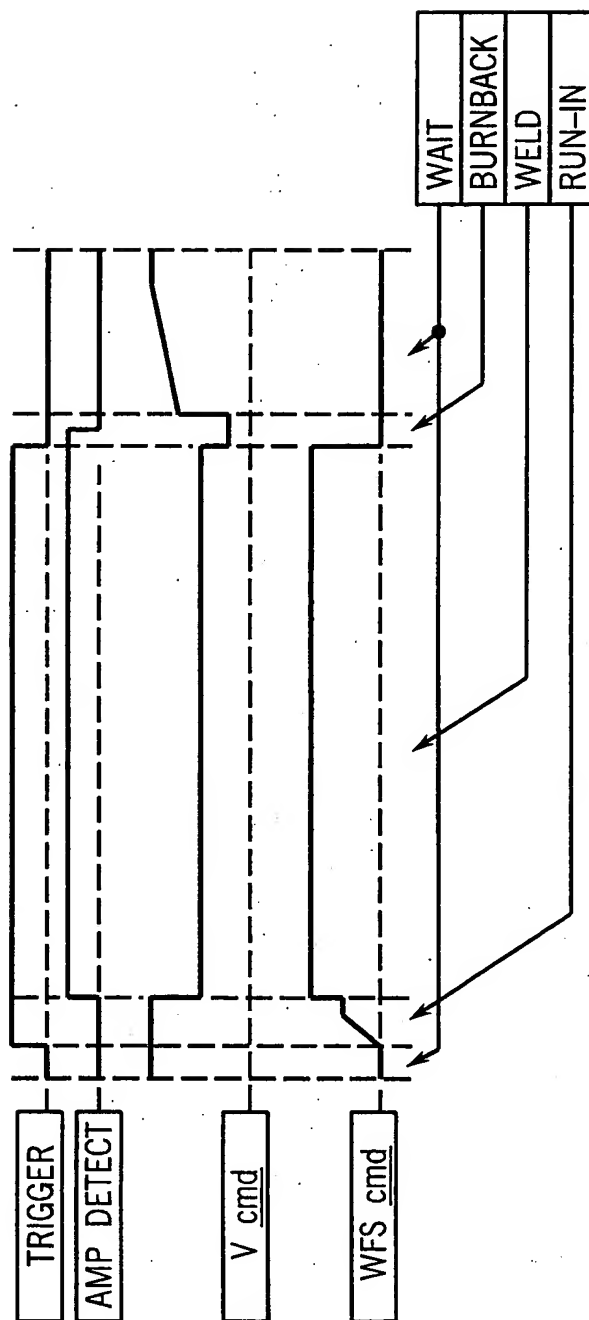


FIG. 2

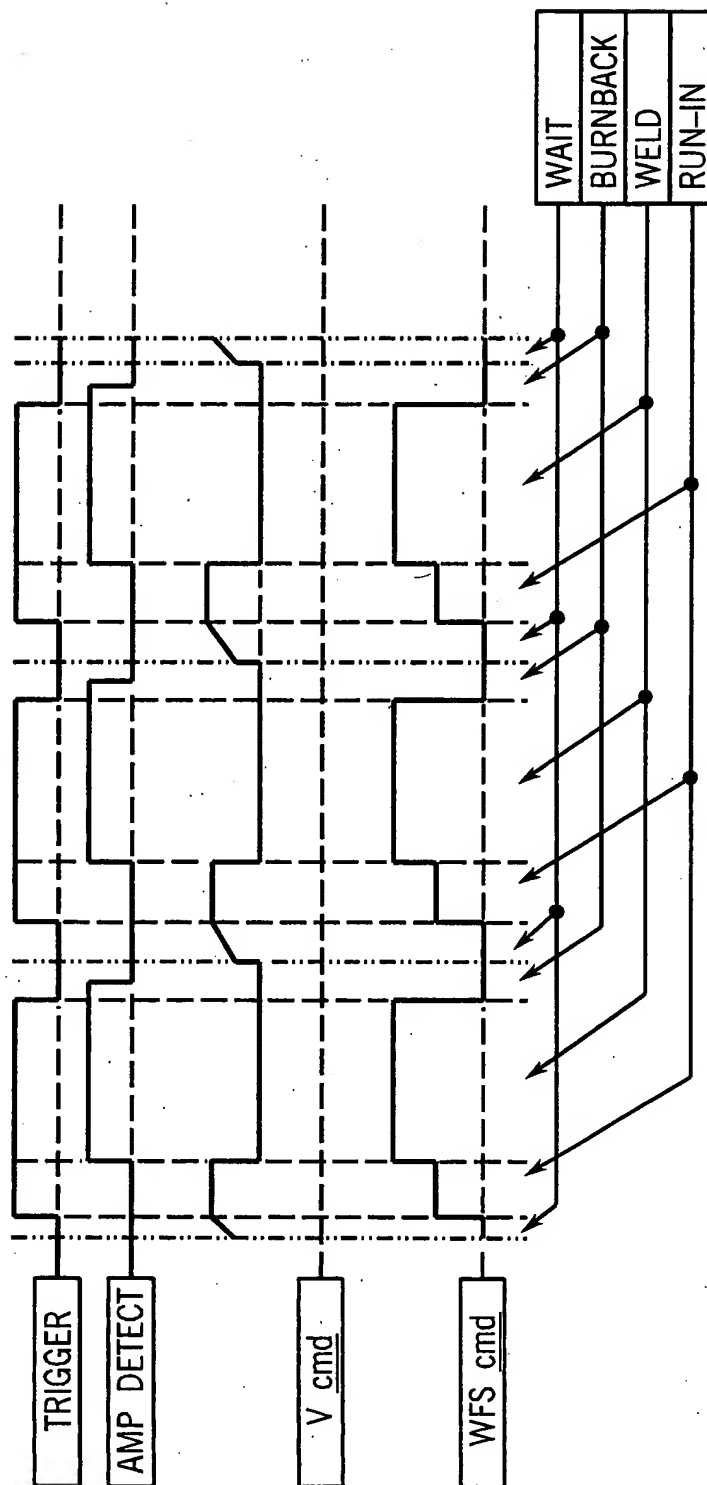


FIG. 3

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